	Type L # Hits	L #	нісв	Search Text	DBs	Time Stamp	Comment	Error Definition	Er ro rs
1	BRS	1.1	1165	(card or cartridge) same cooling and connector	USPA T	USPA 2004/08/1 T 9 12:27			0
2	BRS	1.2	306	1 and @py<1993	USPA T	USPA 2004/08/1 T 9 12:27			0

r tion ro	0	
Error Definition		
Comment		
Time Stamp	2004/08/1 9 10:43	
DBs	USPA	
Text	Н 2	
	("6771509") or ("6771509") or ("6515864") or ("6404639") or ("6025993") or	
Hits	Q	
규 #	L15	*******
Туре	IS&R	
	Н	

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H H R	0	0	0	0
Error Definition				
Comment				
Time Stamp	2004/08/1 9 10:44	2004/08/1 9 10:49	2004/08/1 9 10:50	2004/08/1 9 10:50
DBs	USPA	USPA	1	USPA T
Search Text	("6515864") or ("6404639") or ("6625993") or ("5659459") or ("5615085") or ("5526229") or ("5526229") or ("5475663") or ("5473506") or ("5473506") or ("5432913") or ("5432913") or ("5432913") or ("5289342") or ("5289342") or ("5289342") or ("5188965") or ("5198965") or ("519510") or ("519510") or ("5136464") or ("513646") or ("513646") or ("513644") or ("5016086") or ("5016086") or ("4962444") or ("4962444") or ("4962444") or	(("5526229") or ("5437041")).PN.	("3225262").PN.	("4855869").PN.
Hits	44	е	H	1
т #	L1.7	L18	L19	L20
Type	IS&R	IS&R	IS&R	IS&R
	M	4	5	9

H H H	0	0	0	0	0	0	0
Error Definition							
Comment							
Time Stamp	2004/08/1 9 10:59	2004/08/1 9 11:00	2004/08/1 9 10:57	2004/08/1	2004/08/1	2004/08/1 9 11:03	2004/08/1 9 11:00
DBs	USPA T	USPA T	EPO; JPO; DERW ENT; IBM TDB	EPO; JPO; DERW ENT; IBM TDB	USPA T	USPA T	USPA T
Search Text	((circuit adj board) or substrate) same (cooling same (compress\$3 or resilient or foam or rubber or silicon))	21 and (CPU or processor or microprocessor or ic) same (heat adj sink)	((circuit adj board) or substrate) same (cooling same (compress\$3 or resilient or foam or rubber or silicon))	23 and (CPU or processor or microprocessor or ic) same (heat adj sink)	((circuit adj board) or substrate) same (support\$3 same (compress\$3 or resilient or foam or rubber or silicon))	25 and (CPU or processor or microprocessor or ic) same (heat adj sink)	26 not 22
Hits	3585	8 6 8	1293	თ	20003	112	83
L #	121	L22	1.23	124	1.25	1.26	127
Type	BRS	BRS	BRS	BRS	BRS	BRS	BRS
	7	<b>&amp;</b>	Q	10	11	12	13

Er ro rs	0	0	0	0	0
Error Definition					
Comment					
Time Stamp	2004/08/1 9 11:05	2004/08/1 9 11:06	2004/08/1 9 11:06	2004/08/1 9 11:05	2004/08/1 9 11:06
DBs	US-P GPUB; ; EPO; JPO; DERW ENT; IBM_	EPO; JPO; DERW ENT; IBM TDB	US-P GPUB; EPO; JPO; DERW ENT; IBM_	EPO; JPO; DERW ENT; IBM_ TDB_	USPA T
Search Text	((circuit adj board) or substrate) same (support\$3 same (compress\$3 or resilient or foam or rubber or silicon))	28 and (CPU or processor or microprocessor or ic) same (heat adj sink)	((circuit adj board) or substrate) same (bottom same (compress\$3 or resilient or foam or rubber or silicon))	30 and (CPU or processor or microprocessor or ic) same (heat adj sink)	((circuit adj board) or substrate) same (bottom same (compress\$3 or resilient or foam or rubber or silicon))
Hits	13704	4	10877	ĸ	16598
L #	L28	129	L30	L31	132
Type	BRS	BRS	BRS	BRS	BRS
	14	15	16	17	18

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Er ro rs	0
Error Definition	
Comment	
Time Stamp	USPA 2004/08/1 T 9 11:06
DBs	USPA T
Search Text	32 and (CPU or US processor or US microprocessor or ic) T same (heat adj sink)
Hits	117
# 1	Г.3.3
Type L	BRS
	19

	Туре	#	Hits	Search Text	DBs	Time (Stamp	Comment	Error Definition	ro rs
	IS&R	17	v	(("6771509") or ("6608753") or ("6515864") or ("6404639") or ("6025993") or ("5659459")).PN.	USPA	2004/08/1 8 13:51			0
7	BRS	1.2	10372	((circuit adj board) or substrate) and heat and cooling and spring\$1	USPA	2004/08/1 8 13:52			0
3	BRS	L3	8678	((circuit adj board) or substrate) same (heat adj sink) samespring\$1	USPA T	2004/08/1 8 13:53			0
4	BRS	1.4	529	((circuit adj board) or substrate) same (heat adj sink) same spring\$1	USPA	2004/08/1 8 14:28			0
5	BRS	LS	21454 1	4 and 5-20-92 to present	USPA T	2004/08/1 8 13:58			0
9	BRS	91	529	4 ( 5-20-92 to present).py.	USPA T	2004/08/1 8 14:06			0
7	BRS	17	0	4 and @pd=19920520	USPA T	2004/08/1 8 14:08			0
8	BRS	L8	128	4 and @py<1992	USPA T	2004/08/1 8 14:08			0
6	BRS	1.9	138	4 and @py<1993	USPA T	2004/08/1 8 14:28			0
10	BRS	L11	0	10 and @py<1993	USPA T	2004/08/1 8 14:17			0
11	BRS	L10	289	((circuit adj board) or substrate) same (heat adj sink) same spring\$1	EPO; JPO; DERW ENT; IBM_ TDB_	2004/08/1 8 14:23			0

	Туре	# #	Hits	Search Text	DBs	Time Stamp	Comment	Error Definition	Er ro rs
	BRS	L12	612	((circuit adj board) or substrate) same (heat adj sink) same (pressed or compressed or resilient)	USPA T; EPO; JPO; DERW ENT; IBM_	2004/08/1 8 14:24			0
Щ	BRS	L13	452	12 not 6	USPA T; EPO; JPO; DERW ENT; IBM_	2004/08/1 8 14:24			0
<u>m</u>	BRS	L14	71	13 and @py<1993	USPA T	2004/08/1 8 14:24			0
	BRS	115	1152	((circuit adj board) or substrate) and (heat adj sink) same spring\$1	USPA	2004/08/1 8 14:51			0
Щ	BRS	L16	623	15 not 4	USPA T	2004/08/1 8 14:28			0
Щ	BRS	117	180	16 and @py<1993	USPA T	2004/08/1 8 14:38			0
<u> </u>	BRS	118	362	((circuit adj board) or substrate) and (heat adj sink) same spring\$1	EPO; JPO; DERW ENT; IBM TDB	2004/08/1 8 14:38			0
<u>m</u> 1	BRS	L19	0	18 and @py<1993	USPA	2004/08/1 8 14:38			0

E ro rs	0	0	0		0
Error Definition					
Comment					
Time Stamp	2004/08/1 8 14:46	2004/08/1 8 14:46	2004/08/1 8 14:49	2004/08/1 8 14:50	2004/08/1 8 14:49
DBs	EPO; JPO; DERW ENT; IBM TDB	USPA T; EPO; JPO; DERW ENT; IBM_	USPA T; EPO; JPO; DERW ENT; IBM_	USPA T; EPO; JPO; DERW ENT; IBM_	USPA T; EPO; JPO; DERW ENT; IBM TDB
Search Text	18 and @py<1993	((circuit adj board) or substrate)same processor same spring\$1 same spring\$1	((circuit adj board) or substrate)same processor same spring\$1	22 and @py<1993	((circuit adj board) or substrate)same processor same (resilient or compressible)
Hits	136	175	175	36	51
# 1	1.20	L21	1.22	1.23	1.24
Туре	BRS	BRS	BRS	BRS	BRS
	20	21	2.2	23	24

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Type L # Hits	#			Search Text	DBs	Time Stamp	Comment	Error Definition	H H H
BRS L25 9 24 and @py<1993	9 24	24			USPA T; EPO; JPO; DERW ENT; IBM_	2004/08/1 8 14:51			0
BRS L26 3883 (heat adj board (126 3883 (heat adj sink) and spring\$1	((circuit adj or substrate) (heat adj sink spring\$1	((circuit adj or substrate) (heat adj sink spring\$1	adj ite) sink		USPA	2004/08/1 8 15:24			0
BRS L27 1076 26 and @py<1993	1076 26 and	26 and	and		USPA T; EPO; JPO; DERW ENT; IBM_	2004/08/1 8 15:16			0
BRS L28 896 27 not 17	896 27 not	27 not	not		USPA T; EPO; JPO; DERW ENT; IBM TDB	2004/08/1 8 14:52			0
BRS L29 896 28 not 20	896 28 not	28 not	not		USPA T; EPO; JPO; DERW ENT; IBM TDB	2004/08/1 8 14:53			0

H H B	0	0	0	0	0	0	0	0
Error Definition								
Comment								
Time Stamp	2004/08/1 8 15:16	2004/08/1 8 15:22	2004/08/1 8 15:22	2004/08/1 8 15:25	2004/08/1 8 15:24	2004/08/1 8 15:24	2004/08/1 8 15:25	2004/08/1 8 15:26
DBs	EPO; JPO; DERW ENT; IBM TDB	USPA T; EPO; JPO; DERW ENT; IBM_	USPA T	USPA T; EPO; JPO; DERW ENT; IBM_	USPA T	USPA T		USPA T
Search Text	((circuit adj board) or substrate) and (heat adj sink) and spring\$1	30 and @py<1993	361/704.ccls. and spring\$1	32 and @py<1993	((circuit adj board) 7 or substrate) and cooling and spring\$1	34 not 26	35 and @py<1993	36 and processor
Hits	427	167	622	26	13437	11432	3137	259
T #	1.30	L31	L32	L33	L34	135	136	L37
Туре	BRS	BRS	BRS	BRS	BRS	BRS	BRS	BRS
	30	31	32	33	34	35	36	37

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· 0			2004/08/1 8 14:23	EPO; JPO; DERW ENT; IBM_ TDB_	((circuit adj board) or substrate) same (heat adj sink) same spring\$1	289	L10	BRS	H
0			2004/08/1 8 14:17	USPA T	10 and @py<1993	0	L11	BRS	10
0			2004/08/1 8 14:28	USPA T	4 and @py<1993	138	Г9	BRS	9
0			2004/08/1 8 14:08	USPA T	4 and @py<1992	128	18	BRS	8
0			2004/08/1 8 14:08	USPA T	4 and @pd=19920520	0	L7	BRS	7
0			2004/08/1 8 14:06	USPA T	4 ( 5-20-92 to present).py.	529	16	BRS	6
0			2004/08/1 8 13:58	USPA T	4 and 5-20-92 to present	21454 1	L5	BRS	ហ
0			2004/08/1 8 15:44	USPA T	((circuit adj board) or substrate) same (heat adj sink) same spring\$1	529	L4	BRS	4
0			2004/08/1 8 13:53	USPA T	((circuit adj board) or substrate) same (heat adj sink) samespring\$1	8678	L3	BRS	ω
0			2004/08/1 8 13:52	USPA T	((circuit adj board) or substrate) and heat and cooling and spring\$1	10372	L2	BRS	N
0			2004/08/1 8 13:51	USPA	(("6771509") or ("6608753") or ("6515864") or ("6404639") or ("6025993") or ("5659459")).PN.	0	L1	IS&R	Н
Er ro rs	Error Definition	Comment	Time Stamp	DBs	Search Text	Hits	Ľ #	Туре	

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	Type	#	Hits	Search Text	DBs	Stamp	Comment	Definition	1 70
12	BRS	L12	612	((circuit adj board) or substrate) same (heat adj sink) same (pressed or compressed or resilient)	USPA T; EPO; JPO; DERW ENT; IBM_ TDB_	2004/08/1 8 14:24			0 .
13	BRS	L13	452	12 not 6	USPA T; EPO; JPO; DERW ENT;	2004/08/1			0
14	BRS	L14	71	13 and @py<1993	USPA T	2004/08/1 8 14:24		***************************************	0
15	BRS	L15	1152	((circuit adj board) or substrate) and (heat adj sink) same spring\$1	:	2004/08/1			0
16	BRS	L16	623	15 not 4	USPA T	2004/08/1 8 14:28			0
17	BRS	Ь17	180	16 and @py<1993	USPA T	2004/08/1 8 14:38			0
18	BRS	L18	362	((circuit adj board) or substrate) and (heat adj sink) same spring\$1	EPO; JPO; DERW ENT; IBM_ TDB_	2004/08/1 8 14:38			0
19	BRS	L19	0	18 and @py<1993	USPA T	2004/08/1 8 14:38			0

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		2004/08/1 8 14:49	USPA T; EPO; JPO; DERW ENT; IBM_ IBM_	((circuit adj board) or substrate)same processor same (resilient or compressible)	51	L24	BRS	24
		2004/08/1 8 14:50	USPA T; EPO; JPO; DERW ENT; IBM_ TDB_	22 and @py<1993	3 6	L23	BRS	23
		2004/08/1 8 14:49	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	((circuit adj board) or substrate)same processor same spring\$1	175	L22	BRS	2 2
		2004/08/1 8 14:46	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	((circuit adj board) or substrate)same processor same spring\$1 same spring\$1	175	L21	BRS	21
		2004/08/1 8 14:46	EPO; JPO; DERW ENT; IBM_ TDB_	18 and @py<1993	136	L20	BRS	20
Error Definition	Comment	Time Stamp	DBs	Search Text	Hits	# T	Туре	

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0			2004/08/1 8 14:53	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	28 not 20	896	L29	BRS	29
0			2004/08/1 8 14:52	USPA T; EPO; JPO; DERW ENT; IBM TDB	27 not 17	896	L28	BRS	28
. 0			2004/08/1 8 15:16	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	26 and @py<1993	1076	L27	BRS	27
0			2004/08/1 8 15:24	USPA	((circuit adj board) or substrate) and (heat adj sink) and spring\$1	3883	L26	BRS	26
0			2004/08/1 8 14:51	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	24 and @py<1993	ν	L25	BRS	N U
us uo E	Error Definition	Comment	Time Stamp	DBs	Search Text	Hits	L #	Туре	

	Туре	L#	Hits	Search Text	DBs	Time Stamp	Comment	Error Definition	rs ro
30	BRS	L30	427	((circuit adj board) or substrate) and (heat adj sink) and spring\$1	EPO; JPO; DERW ENT; IBM_ TDB_	2004/08/1 8 15:16			0
31	BRS	L31	167	30 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ IDB_	2004/08/1 8 15:22			0
32	BRS	L32	622	361/704.ccls. and spring\$1	USPA T	2004/08/1 8 15:22			0
ယ္သ	BRS	L33	Q 2	32 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:25			0
34	BRS	L34	13437	((circuit adj board) or substrate) and cooling and spring\$1	USPA T	2004/08/1 8 15:24			0
35	BRS	L35	11432	34 not 26	USPA T	2004/08/1 8 15:24		,	0
36	BRS	L36	3137	35 and @py<1993	T T	2004/08/1 8 15:25			0
37	BRS	L37	259	36 and processor	USPA T	2004/08/1 8 15:26			0
38	BRS	L38	9	((circuit adj board) or substrate) same cooling same foam same housing	USPA T	2004/08/1 8 15:50			0

	Туре	۲ #	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	7 H H
39	BRS	L39	Ν	((circuit adj board) or substrate) same cooling same foam same housing	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:46			0
40	BRS	L40	ν	((circuit adj board) or substrate) same cooling same (compressible or compressed) same housing	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:48			0
41	BRS	L41	σ	((circuit adj board) or substrate) same cooling same (compressible or compressed) same housing	USPA	2004/08/1			0
42	BRS	L42	0	((circuit adj board) or substrate) same cooling same (silicon same rubber)same housing	USPA T	2004/08/1			0
43	BRS	L43	0	((circuit adj board) or substrate) same cooling same (silicon same rubber) same housing	USPA T	2004/08/1 8 15:51			0
44	BRS	L44	24	((circuit adj board) or substrate) same cooling same (rubber) same housing	USPA T	2004/08/1 8 15:52			0
45	BRS	L45	12	((circuit adj board) or substrate) same between same compressible same processor	USPA	2004/08/1 8 15:54			0

	Туре	#	Hits	ch Te	DBs	Time Stamp		Comment
46	BRS	L46	H	((circuit adj board) or substrate) same between same compressible same processor	,	200	2004/08/1 8 15:55	04/08/1 15:55
47	BRS	L47	ω	((circuit adj board) or substrate) same between same resilient same processor	EPO; JPO; DERW ENT; IBM_ TDB_	200	04/08/1 L5:55	2004/08/1 8 15:55
44 80	BRS	L48	13	((circuit adj board) or substrate) same between same resilient same processor	USPA	8 2	2004/08/1 8 15:56	04/08/1 15:56
49	BRS	L49	62	((circuit adj board) or substrate) same between same spring same processor	USPA T	200	2004/08/1 8 15:58	)4/08/1 L5:58
50	BRS	L50	18	((circuit adj board) or substrate) same between same spring same processor	EPO; JPO; DERW ENT; IBM_ TDB_	200	)4/08/1 L6:00	2004/08/1 8 16:00
51	BRS	Ľ51	<u>س</u>	((circuit adj board) or substrate) same support same spring same processor	EPO; JPO; DERW ENT; IBM_ TDB_	200 8 1	2004/08/1 8 16:01	
52	BRS	L52	Н	((circuit adj board) or substrate) same support same (rubber or foam) same processor	EPO; JPO; DERW ENT; IBM_ TDB_	200	2004/08/1 8 16:02	4/08/1 6:02

		2004/08/1 8 16:03	T; T; EPO; JPO; DERW ENT; IBM TDB	((circuit adj board) or substrate) same support same springs same processor		L53 21	BRS	ຫ ຜ
Error Definition	Comment s	Time Stamp	DBs	Search Text	Type L # Hits	# 1	Туре	